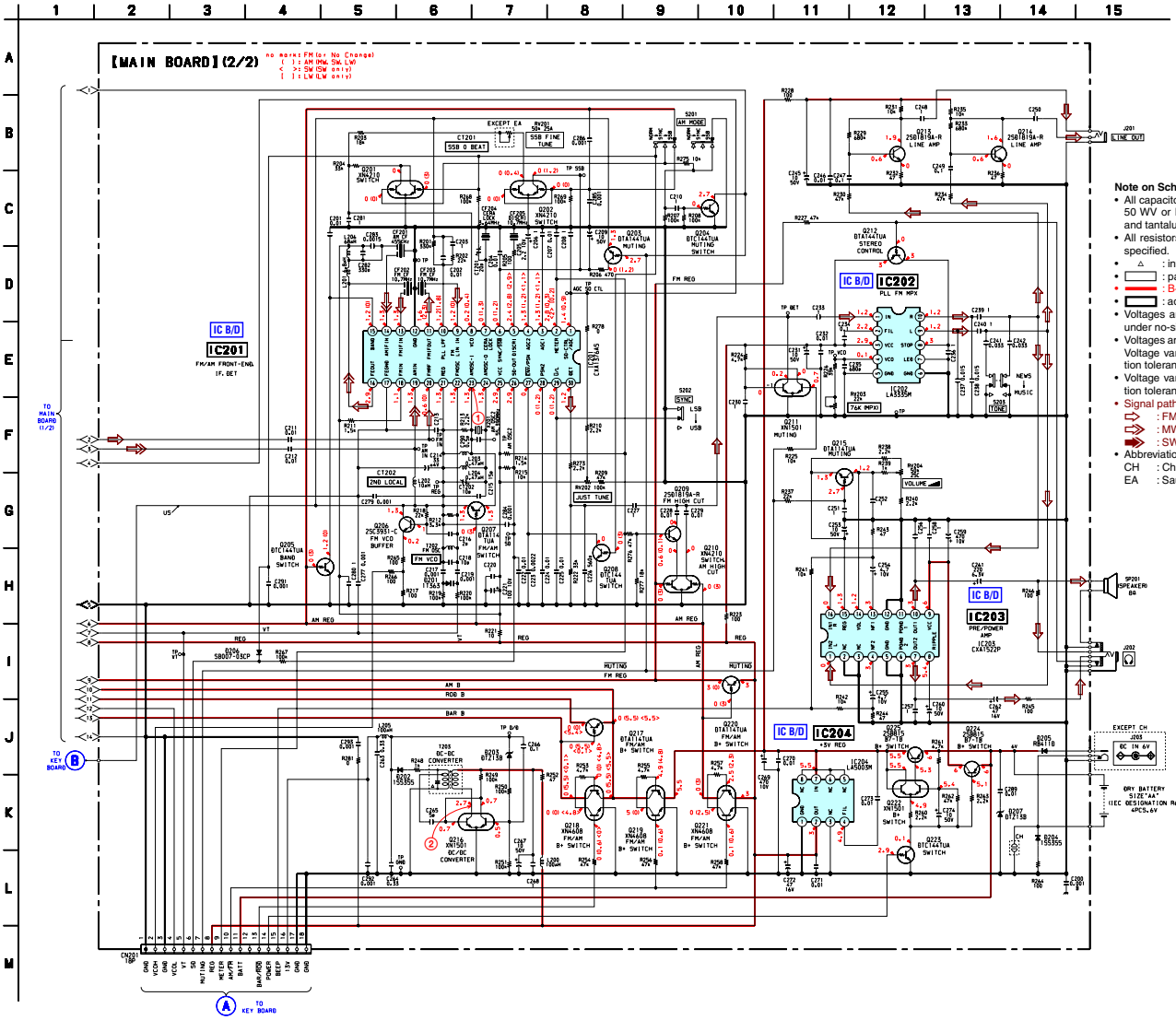
4-5. SCHEMATIC DIAGRAM – MAIN BOARD (2/2) – • Refer to page 16 for IC Block Diagrams.

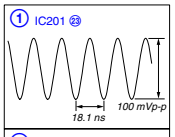


Note on Schematic Diagram:

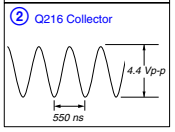
- All capacitors are in µF unless otherwise noted. pF: µµF 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and 1/4 W or less unless otherwise specified.
- △ : internal component.
- ▭ : panel designation.
- ▬ : B+ Line.
- ▭ : adjustment for repair.
- Voltages and waveforms are dc with respect to ground under no-signal (detuned) conditions.
- Voltages are taken with a VOM (Input impedance 10 MΩ). Voltage variations may be noted due to normal production tolerances.
- Voltage variations may be noted due to normal production tolerances.
- Signal path.
  ⇨ : FM
  ⇨ : MW/LW
  ⇨ : SW
- Abbreviation
  CH : Chinese model
  EA : Saudi Arabia model

• WAVEFORMS

① IC201 ㉓



② Q216 Collector



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